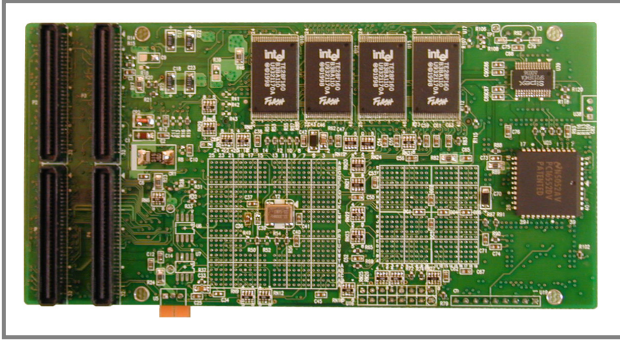


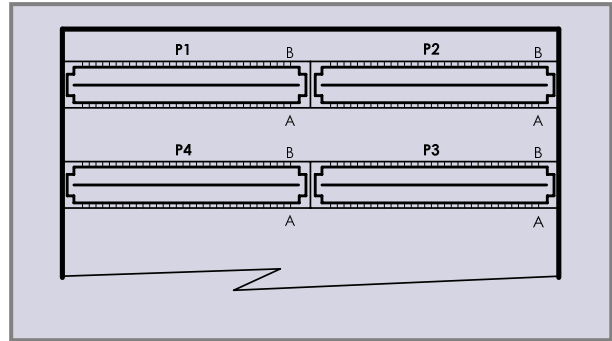
AP755 PMC PIN Diagram

PCI Mezzanine Card (PMC)

AP755PMC Bottom Side



AP755PMC Connector Pin Diagram



AP755PMC Board Connector Pin Map

P1 Connector

Col	Name	Interface	Description
A1	SDMA(0)	Memory	Local Addr 0
A2	SDMA(1)	Memory	Local Addr 1
A3	SDMA(2)	Memory	Local Addr 2
A4	SDMA(3)	Memory	Local Addr 3
A5	SDMA(4)	Memory	Local Addr 4
A6	SDMA(5)	Memory	Local Addr 5
A7	SDMA(6)	Memory	Local Addr 6
A8	SDMA(7)	Memory	Local Addr 7
A9	SDMA(8)	Memory	Local Addr 8
A10	SDMA(9)	Memory	Local Addr 9
A11	SDMA(10)	Memory	Local Addr 10
A12	SDBA(0)	Memory	Local Addr 11
A13	PAR(7)	Memory	Local Addr 12
A14	PAR(6)	Memory	Local Addr 13
A15	PAR(5)	Memory	Local Addr 14
A16	PAR(4)	Memory	Local Addr 15
A17	PAR(3)	Memory	Local Addr 16
A18	PAR(2)	Memory	Local Addr 17
A19	PAR(1)	Memory	Local Addr 18
A20	PAR(0)	Memory	Local Addr 19
A21	SDBA(1)	Memory	Local Addr 20
A22	SDMA(11)	Memory	Local Addr 21
A23	SDMA(12)	Memory	Local Addr 22
A24	SDMA(13)	Memory	Local Addr 23
A25	RCS(0)	Memory	ROM/Bank 0 Sel
A26	RCS(1)	Memory	ROM/Bank 1 Sel
A27	RCS(2)	Memory	ROM/Bank 2 Sel
A28	RCS(3)	Memory	ROM/Bank 3 Sel
A29	FOE	Memory	Flash Output Enable
A30	WE	Memory	Write Enable
A31	AS	Memory	Address Strobe
A32	IRQ(4)	EPIC	Interrupt 4

Col	Name	Interface	Description
B1	MDH(0)	Memory	Data Bus High 0
B2	MDH(1)	Memory	Data Bus High 1
B3	MDH(2)	Memory	Data Bus High 2
B4	MDH(3)	Memory	Data Bus High 3
B5	MDH(4)	Memory	Data Bus High 4
B6	MDH(5)	Memory	Data Bus High 5
B7	MDH(6)	Memory	Data Bus High 6
B8	MDH(7)	Memory	Data Bus High 7
B9	-		
B10	-		
B11	RAMLED	Peripheral	LED
B12	MDH(8)	Memory	Data Bus High 8
B13	MDH(9)	Memory	Data Bus High 9
B14	MDH(10)	Memory	Data Bus High 10
B15	MDH(11)	Memory	Data Bus High 11
B16	MDH(12)	Memory	Data Bus High 12
B17	MDH(13)	Memory	Data Bus High 13
B18	MDH(14)	Memory	Data Bus High 14
B19	MDH(15)	Memory	Data Bus High 15
B20	JTS		
B21	-		
B22	SDA		I2C DATA
B23	SCL		I2C Clock
B24	-		
B25	RTS	Peripheral	Request to Send
B26	CTS	Peripheral	Clear to Send
B27	Uart_TX	Peripheral	UART A Ch Transmit
B28	Uart_RX	Peripheral	UART A Ch Receive
B29	-		
B30	-		
B31	-		
B32	-		

P2 Connector

Col	Name	Interface	Description
A1	-		
A2	-		
A3	-		
A4	GND		
A5	-		
A6	REQ(1)	PCI	PCI Bus Request(1)
A7	PCIRST	PCI	PCI Reset
A8	VCC(3.3V)		
A9	-		
A10	AD(30)	PCI	Address/Data
A11	GND		
A12	AD(24)	PCI	Address/Data
A13	IDSEL	PCI	ID Select
A14	VCC(3.3V)		
A15	AD(18)	PCI	Address/Data
A16	AD(16)	PCI	Address/Data
A17	GND		

Col	Name	Interface	Description
B1	-		
B2	-		
B3	GND		
B4	-		
B5	-		
B6	VCC(3.3V)		
B7	REQ(2)	PCI	PCI Bus Request(2)
B8	REQ(3)	PCI	PCI Bus Request(3)
B9	GND		
B10	AD(29)	PCI	Address/ Data
B11	AD(26)	PCI	Address/ Data
B12	VCC		
B13	AD(23)	PCI	Address/ Data
B14	AD(20)	PCI	Address/Data
B15	GND		
B16	C/BE(2)	PCI	Com/Byte Enable(2)
B17	-		

P2 Connector

Col	Name	Interface	Description
A18	TRDY	PCI	Target Ready
A19	GND		
A20	PERR	PCI	Parity Error
A21	VCC(3.3V)		
A22	C/BE(1)	PCI	Com/Byte Enable(1)
A23	AD(14)	PCI	Address/Data
A24	M66EN	PCI	66Mhz PCI Allowed
A25	AD(8)	PCI	Address/Data
A26	AD(7)	PCI	Address/Data
A27	VCC(3.3V)		
A28	GNT(1)	PCI	PCI Bus Grant(1)
A29	GNT(2)	PCI	PCI Bus Grant(2)
A30	GND		
A31	-		
A32	GND		

Col	Name	Interface	Description
B18	VCC(3.3V)		
B19	STOP	PCI	Stop
B20	GND		
B21	SERR	PCI	System Error
B22	GND		
B23	AD(13)	PCI	Address/ Data
B24	AD(10)	PCI	Address/ Data
B25	VCC		
B26	-		
B27	-		
B28	GND		
B29	GNT(3)	PCI	PCI Bus Grant(3)
B30	SYSRST		System Reset
B31	VCC		
B32	SYSCON		Hardware Reset

P3 Connector

Col	Name	Interface	Description
A1	REQ(4)		PCI Bus Request(4)
A2	GND		
A3	IRQ(1)	EPCL	Interrupt1
A4	GND		
A5	IRQ(3)	EPCL	Interrupt3
A6	GND		
A7	PCICLK	Clock	PCI Clock Input
A8	GND		
A9	GNT(0)	PCI	PCI Bus Grant(0)
A10	VCC(3.3V)		
A11	AD(28)	PCI	Address/Data
A12	AD(25)	PCI	Address/Data
A13	GND		
A14	AD(22)	PCI	Address/Data
A15	AD(19)	PCI	Address/Data
A16	VCC(3.3V)		
A17	FRAME	PCI	Frame
A18	GND		
A19	DEVSEL	PCI	Device Select
A20	GND		
A21	GNT(4)		PCI BUS Grant(4)
A22	PAR	PCI	Parity
A23	VCC(3.3V)		
A24	AD(12)	PCI	Address/Data
A25	AD(9)	PCI	Address/Data
A26	GND		
A27	AD(6)	PCI	Address/Data
A28	AD(4)	PCI	Address/Data
A29	VCC(3.3V)		
A30	AD(2)	PCI	Address/Data
A31	AD(0)	PCI	Address/Data
A32	GND		

Col	Name	Interface	Description
B1	-		
B2	IRQ(0)	EPCL	Interrupt0
B3	IRQ(2)	EPCL	Interrupt2
B4	VCC(5V)		
B5	-		
B6	-		
B7	GND		
B8	REQ(0)	PCI	PCI Bus Request(0)t
B9	VCC(5V)		
B10	AD(31)	PCI	Address/Data
B11	AD(27)	PCI	Address/Data
B12	GND		
B13	C/BE(3)	PCI	Com/Byte Enable(3)
B14	AD(21)	PCI	Address/Data
B15	VCC(5V)		
B16	AD(17)	PCI	Address/Data
B17	GND		
B18	IRDY	PCI	Initiator Ready
B19	VCC(5V)		
B20	LOCK	PCI	Lock
B21	-		
B22	GND		
B23	AD(15)	PCI	Address/Data
B24	AD(11)	PCI	Address/Data
B25	VCC(5V)		
B26	C/BE(0)	PCI	Com/byte enable(0)
B27	AD(5)	PCI	Address/Data
B28	GND		
B29	AD(3)	PCI	Address/Data
B30	AD(1)	PCI	Address/Data
B31	VCC(5V)		
B32	-		

P4 Connector

Col	Name	Interface	Description
A1	MDH(16)	Memory	Data Bus High 16
A2	MDH(17)	Memory	Data Bus High 17
A3	MDH(18)	Memory	Data Bus High 18
A4	MDH(19)	Memory	Data Bus High 19
A5	MDH(20)	Memory	Data Bus High 20
A6	MDH(21)	Memory	Data Bus High 21
A7	MDH(22)	Memory	Data Bus High 22
A8	MDH(23)	Memory	Data Bus High 23
A9	GND		
A10	GND		
A11	MDH(24)	Memory	Data Bus High 24
A12	MDH(25)	Memory	Data Bus High 25
A13	MDH(26)	Memory	Data Bus High 26
A14	MDH(27)	Memory	Data Bus High 27
A15	MDH(28)	Memory	Data Bus High 28
A16	MDH(29)	Memory	Data Bus High 29
A17	MDH(30)	Memory	Data Bus High 30
A18	MDH(31)	Memory	Data Bus High 31
A19	GND		
A20	GND		
A21	-		
A22	-		
A23	-		
A24	-		
A25	-		
A26	-		
A27	-		
A28	-		
A29	SDA	I2C Control	Serial Data
A30	SCL	I2C Control	Serial Clock
A31	GND		
A32	GND		

Col	Name	Interface	Description
B1	GND		
B2	-		
B3	VCC(5)		
B4	VCC(5)		
B5	VCC(5)		
B6	VCC(5)		
B7	VCC(5)		
B8	-		
B9	GND		
B10	GND		
B11	-		
B12	-		
B13	-		
B14	-		
B15	GND		
B16	GND		
B17	GND		
B18	GND		
B19	GND		
B20	GND		
B21	-		
B22	VCC(3.3V)		
B23	VCC(3.3V)		
B24	VCC(3.3V)		
B25	VCC(3.3V)		
B26	VCC(3.3V)		
B27	VCC(3.3V)		
B28	-		
B29	-		
B30	-		
B31	GND		
B32	GND		